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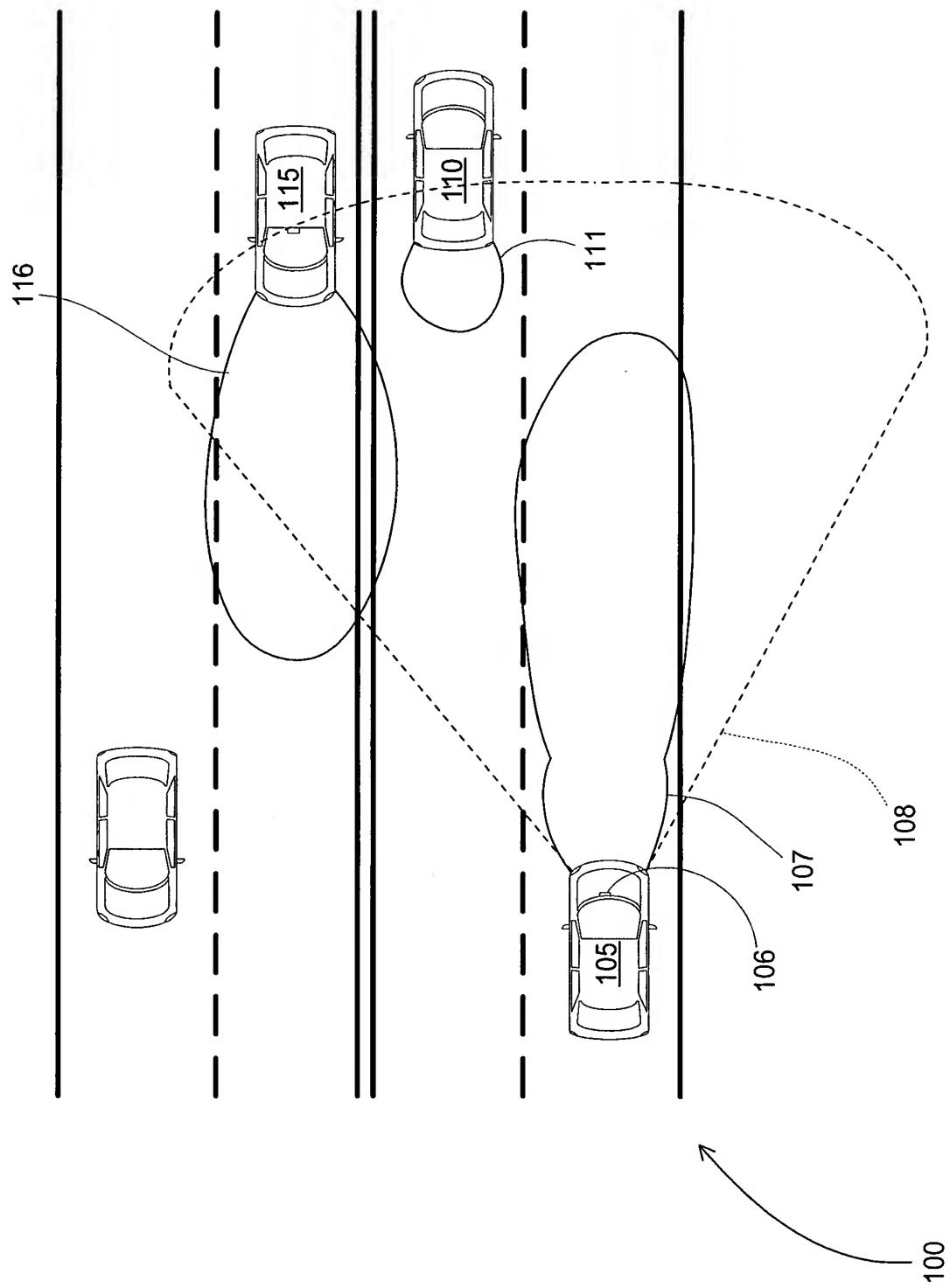
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**Fig. 1**

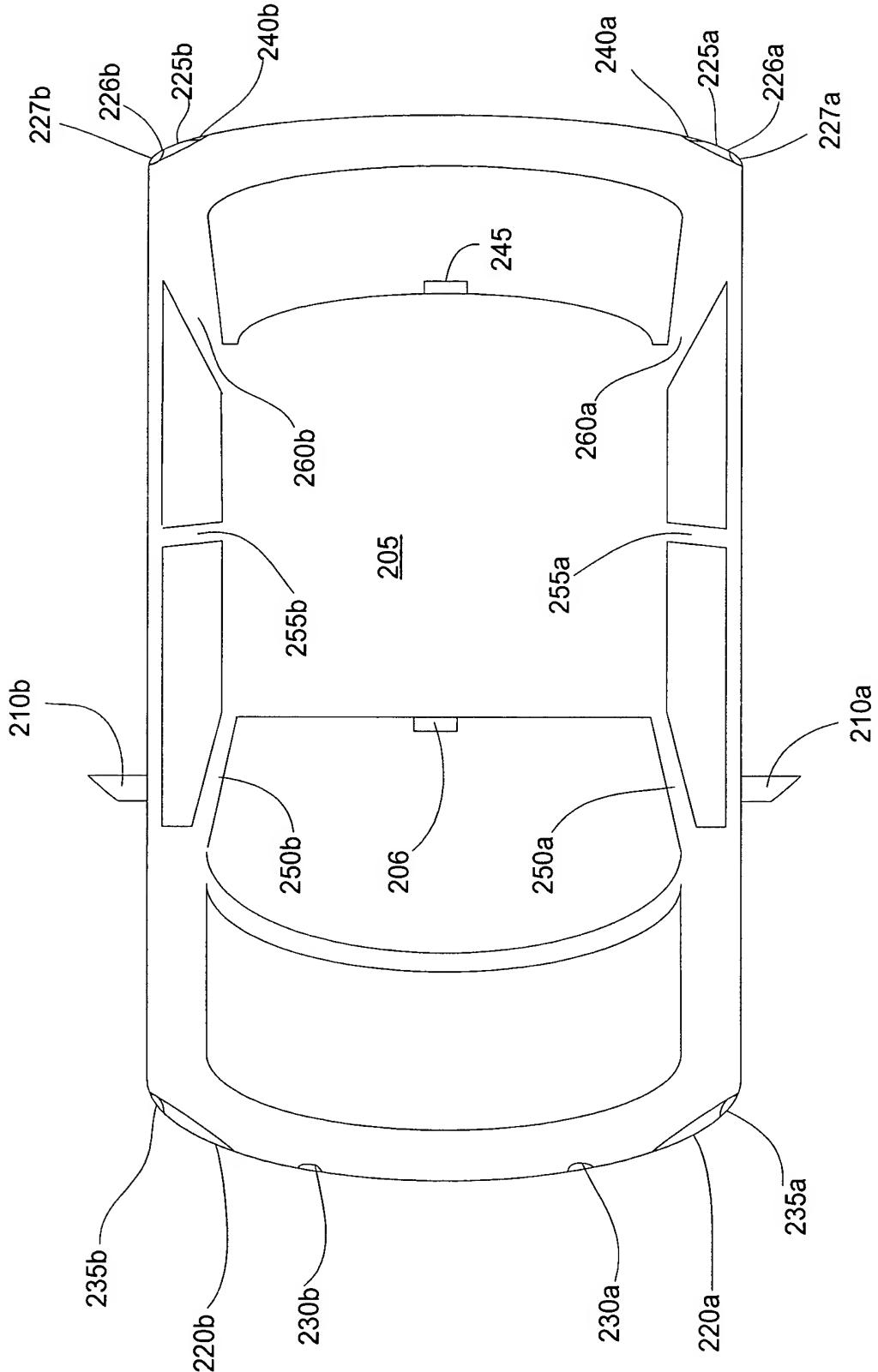


Fig. 2

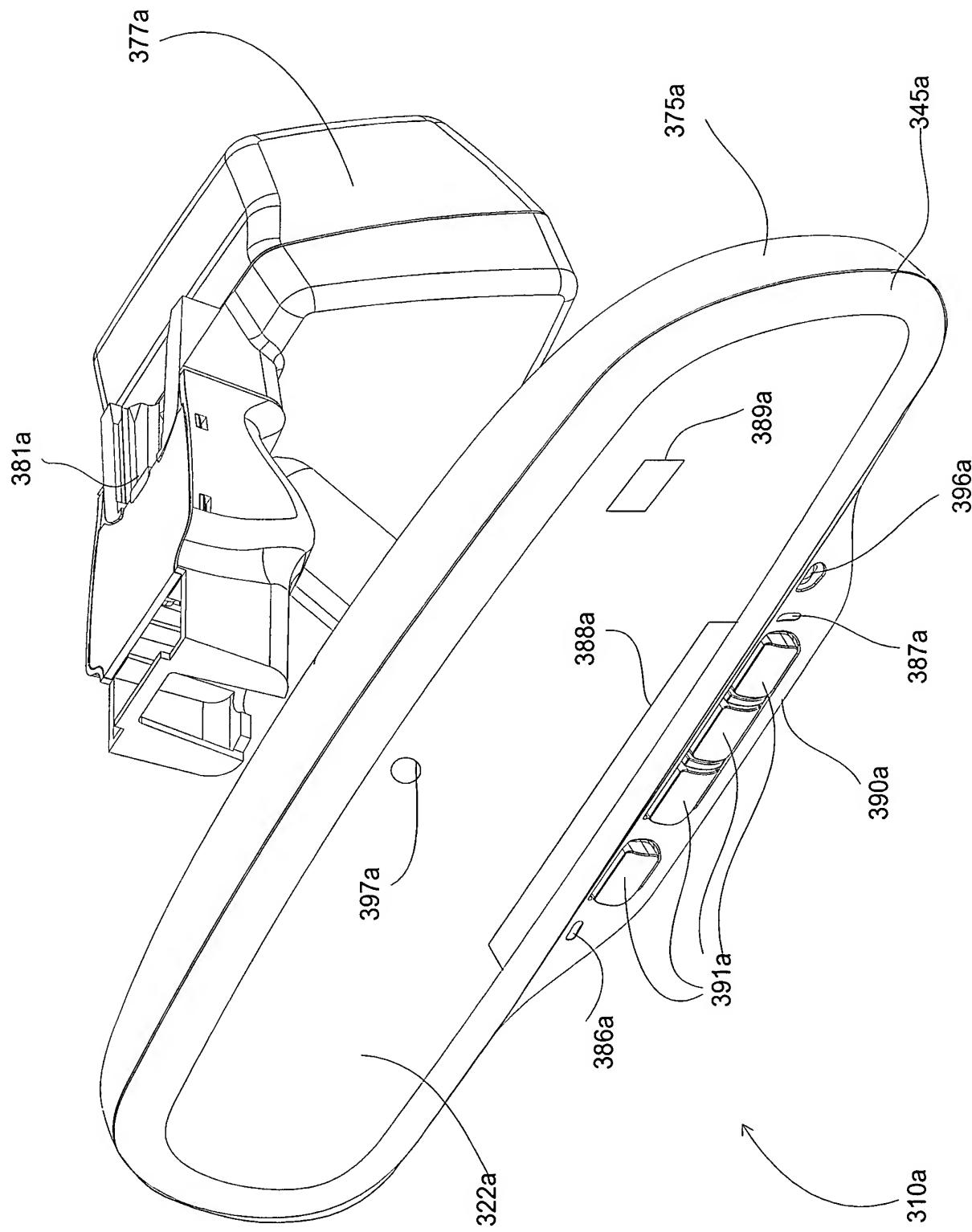


Fig. 3a

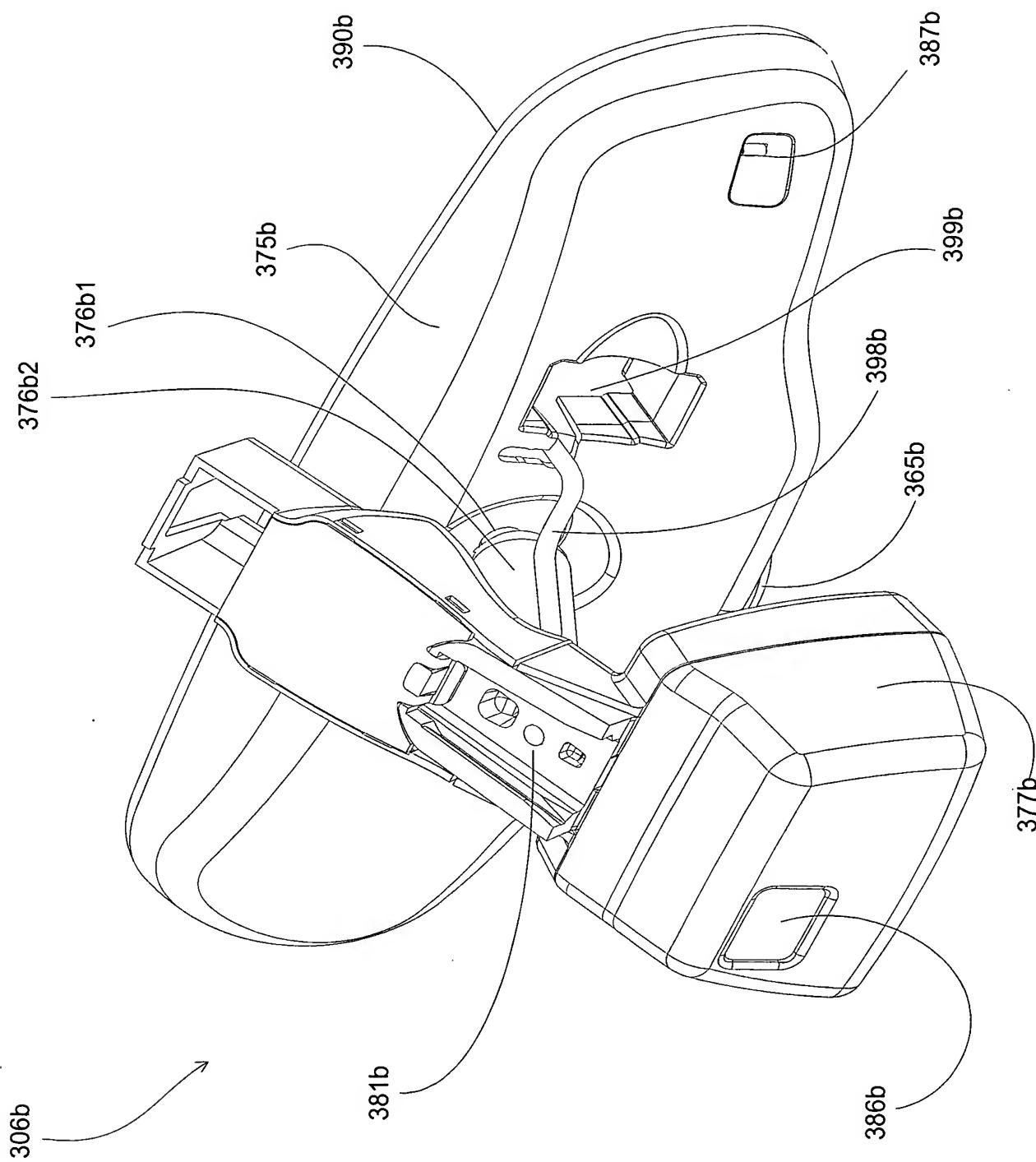


Fig. 3b

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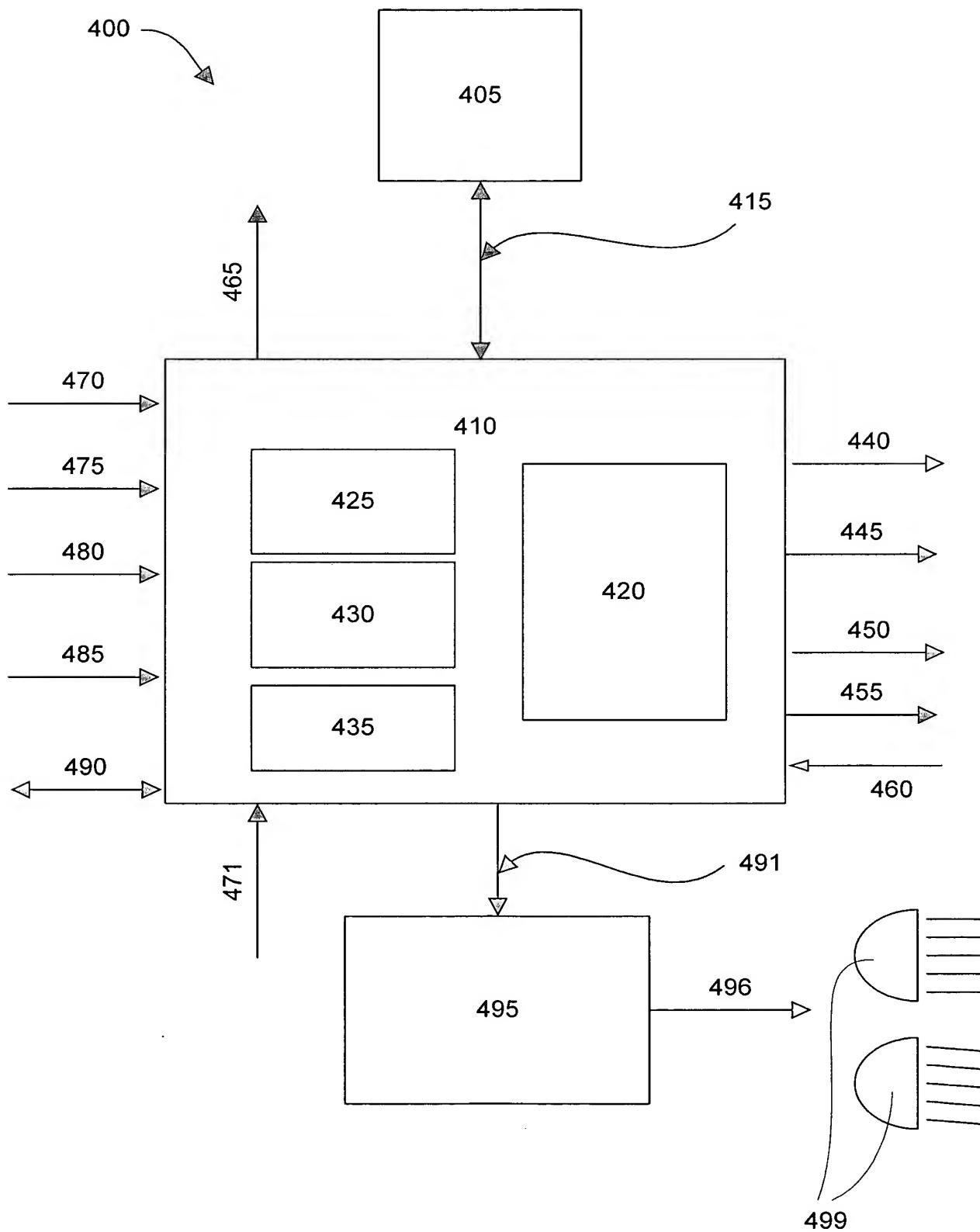


Fig.4

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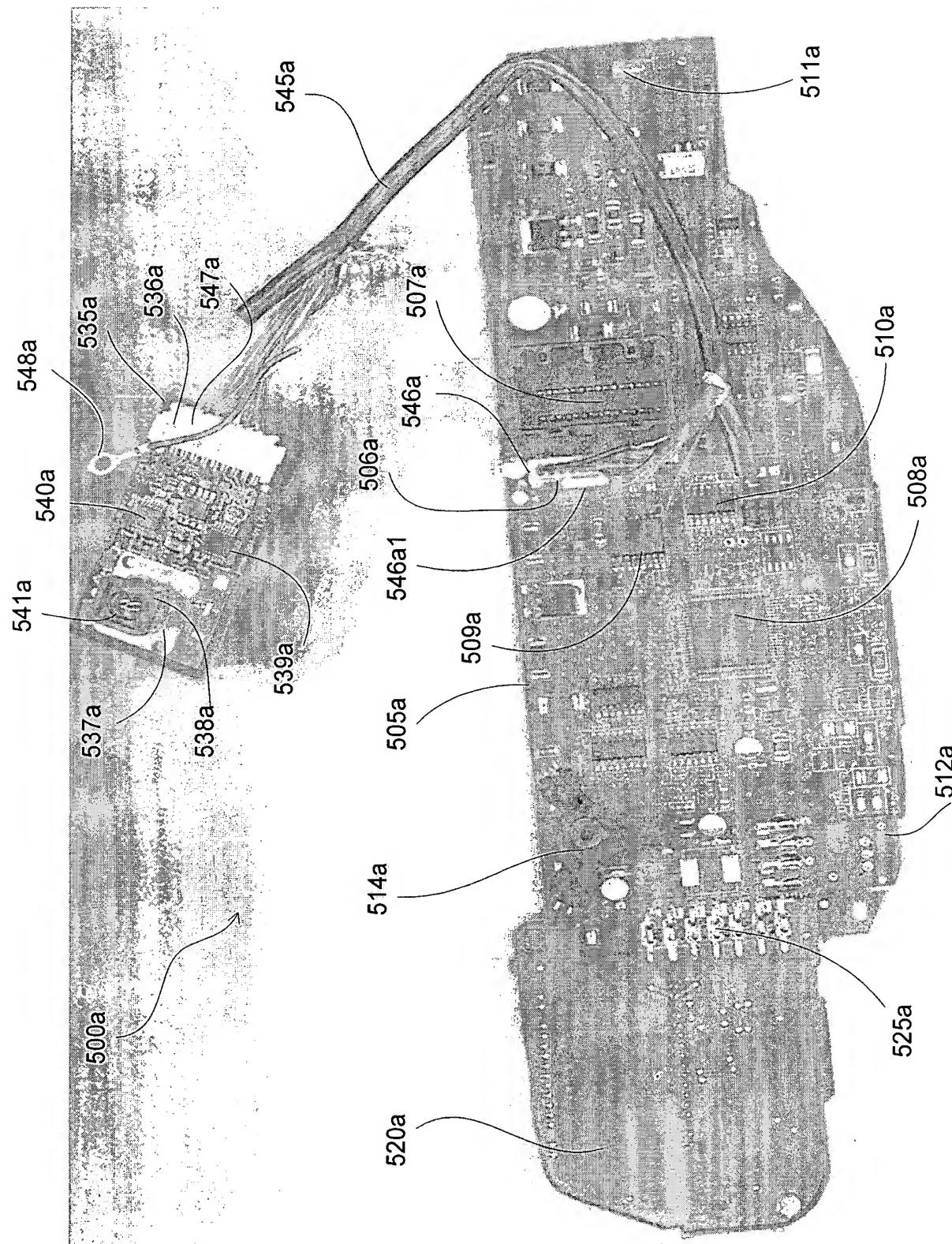


Fig.5a

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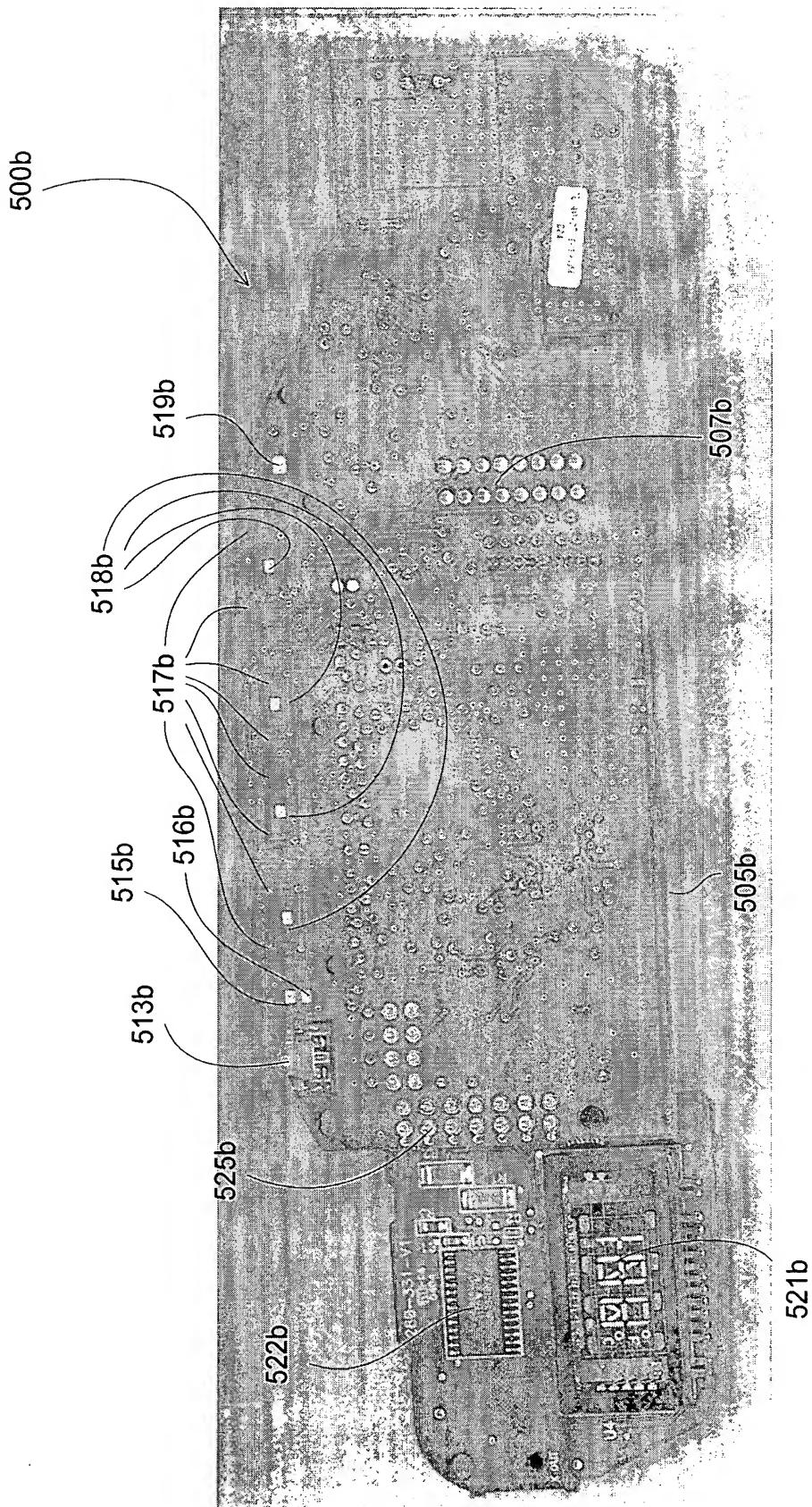


Fig.5b

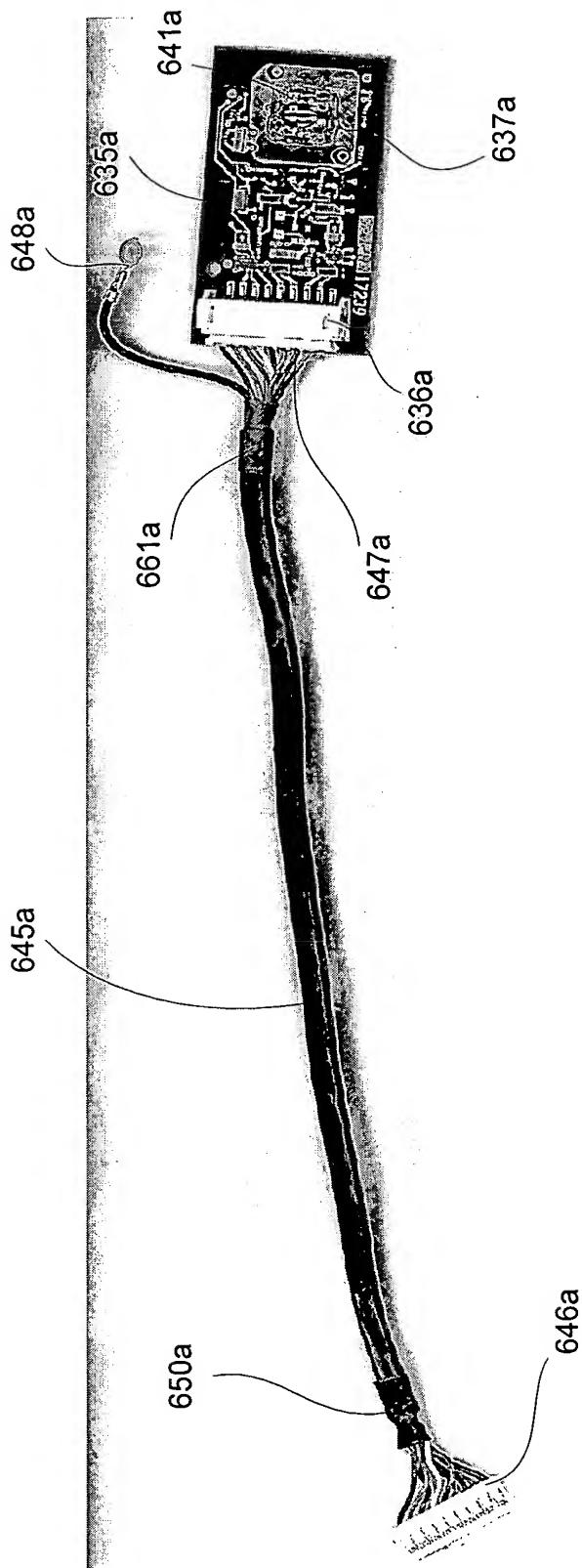
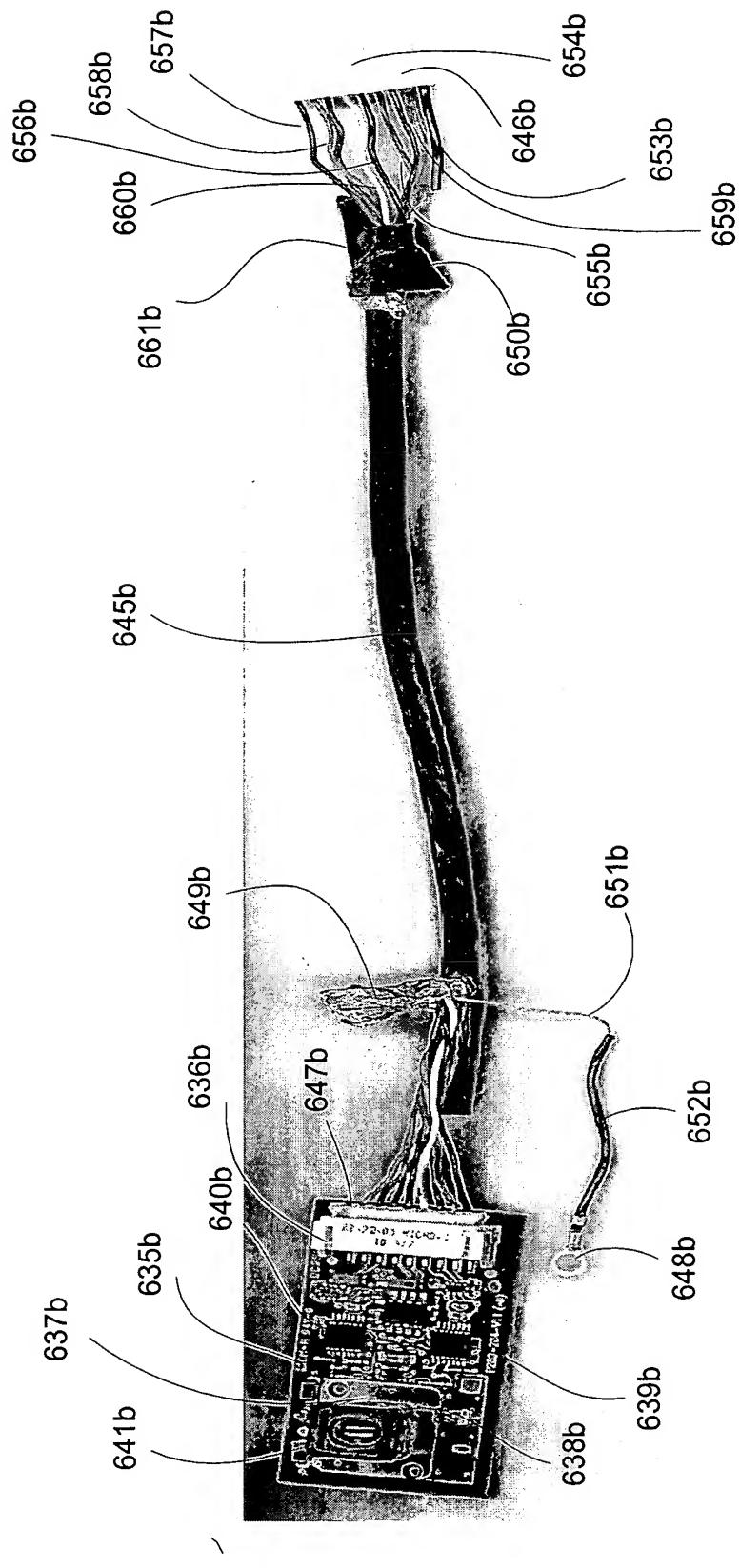


Fig.6a

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**Fig.6b**

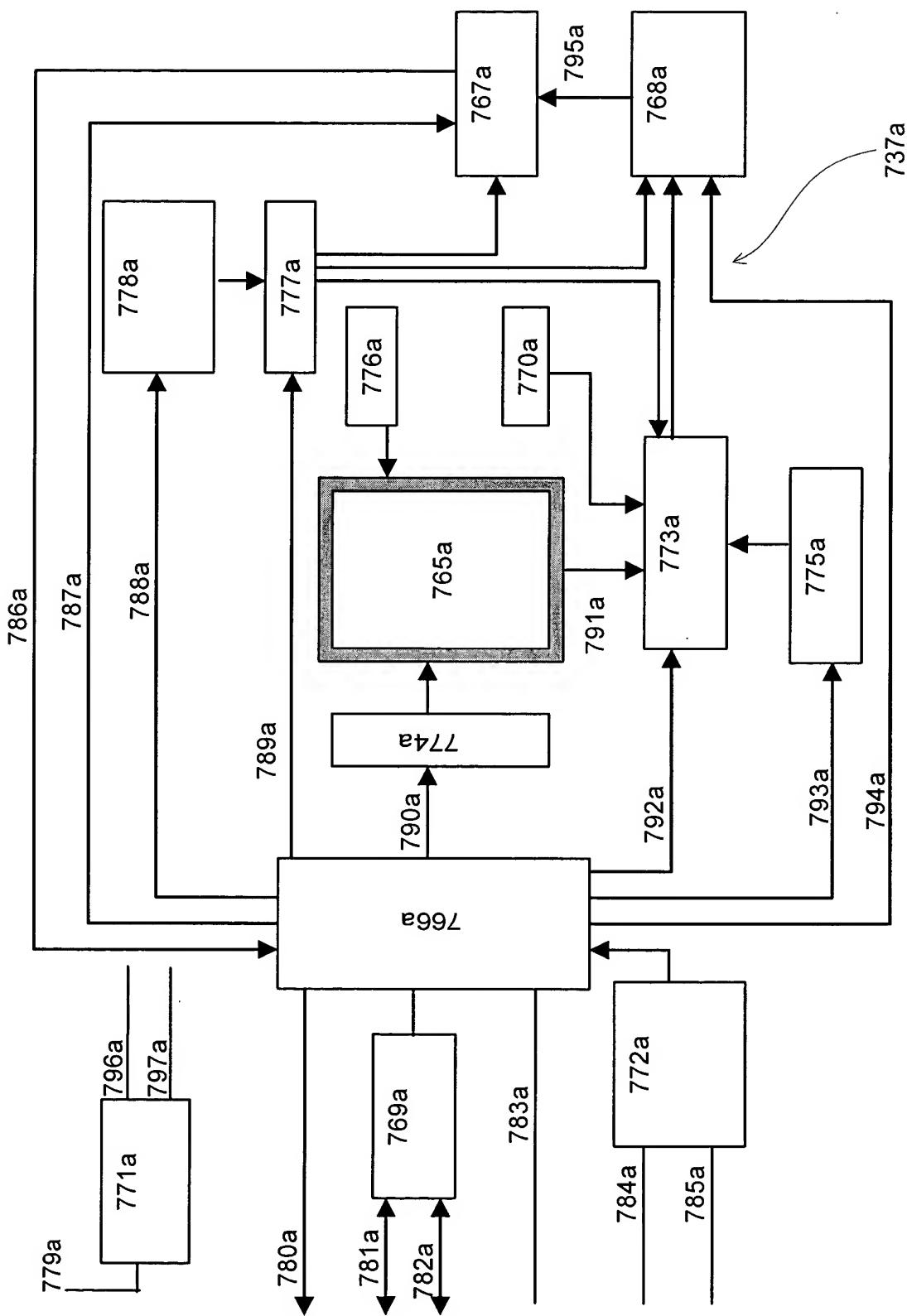
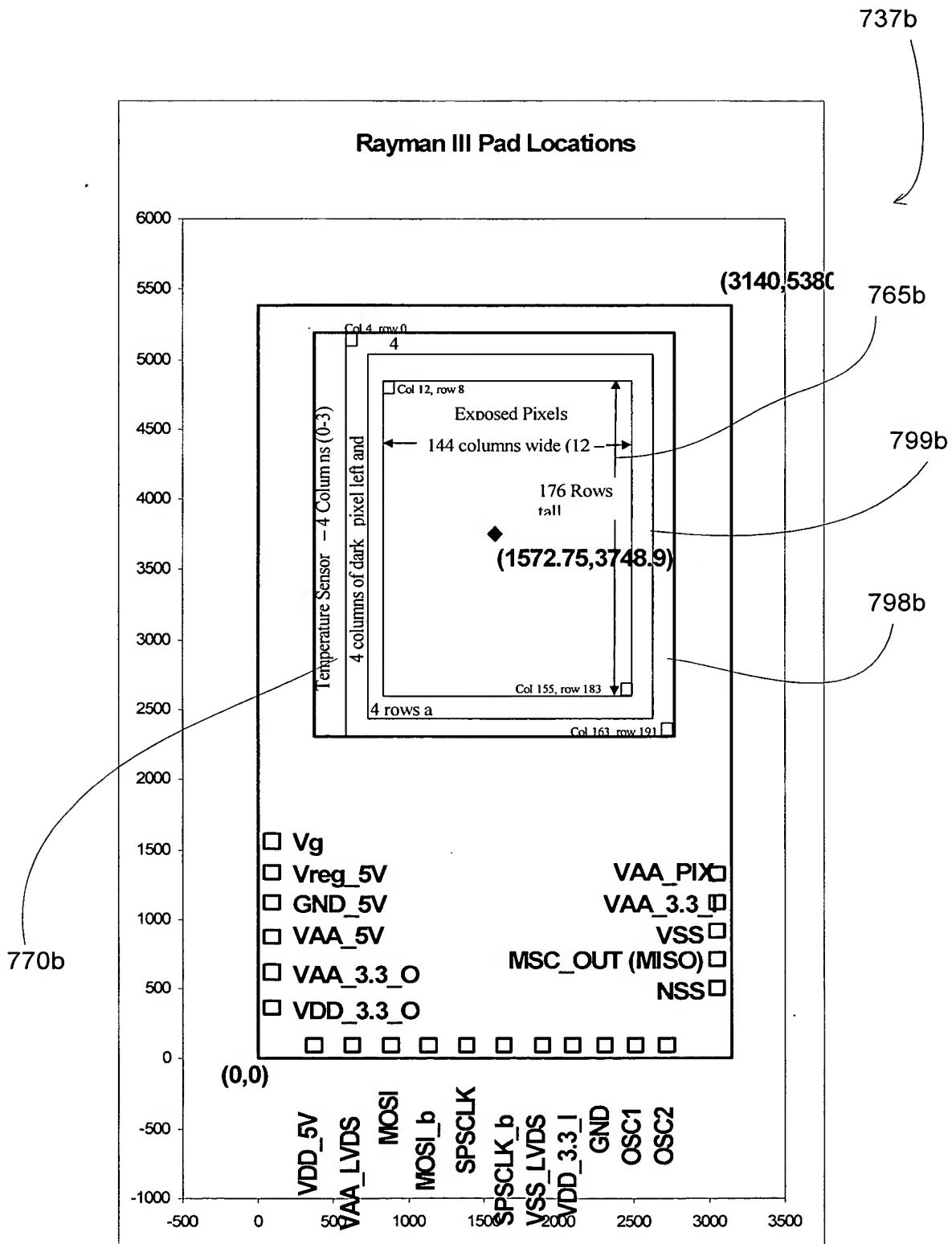


Fig.7a

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**Fig.7b**

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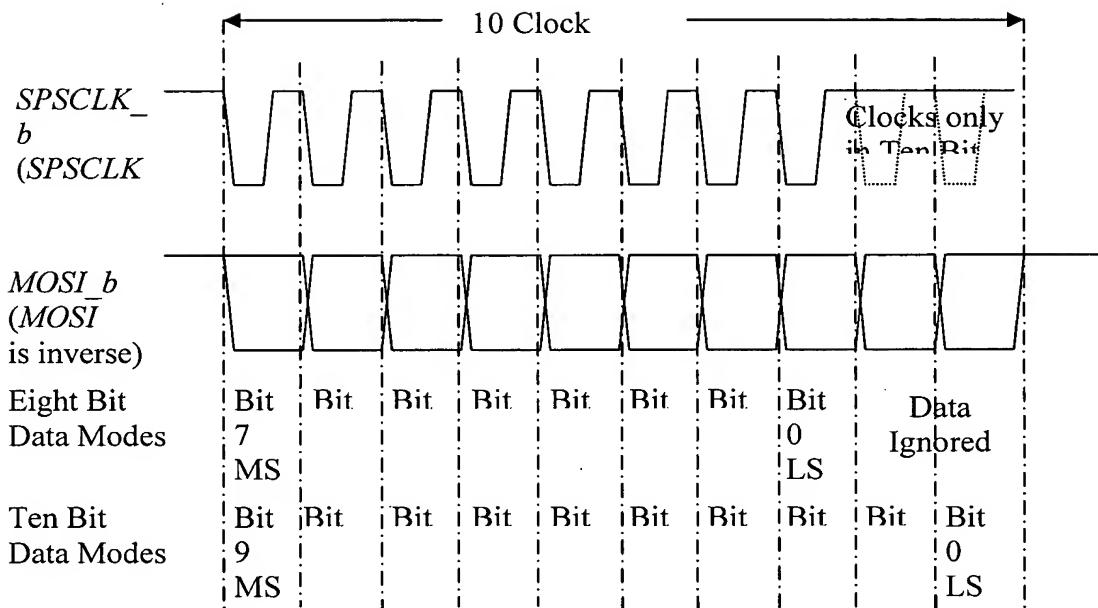


Fig.7c

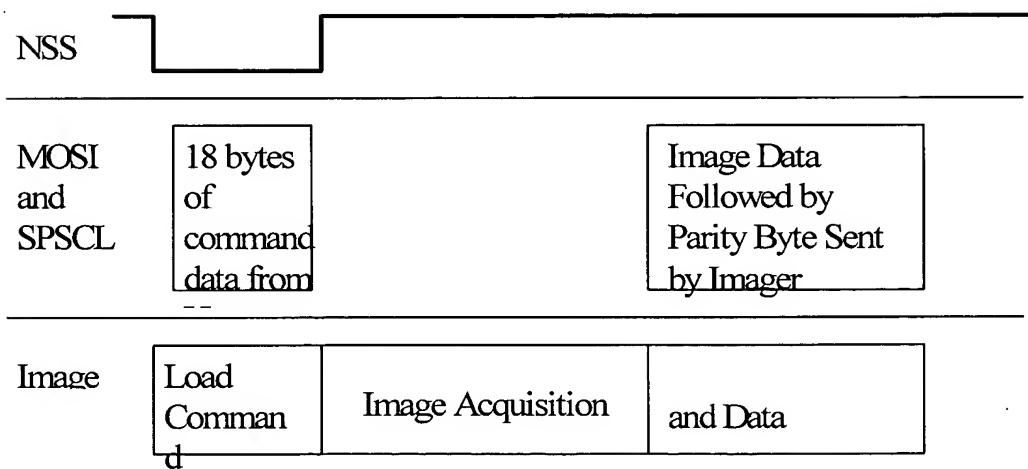


Fig.7d

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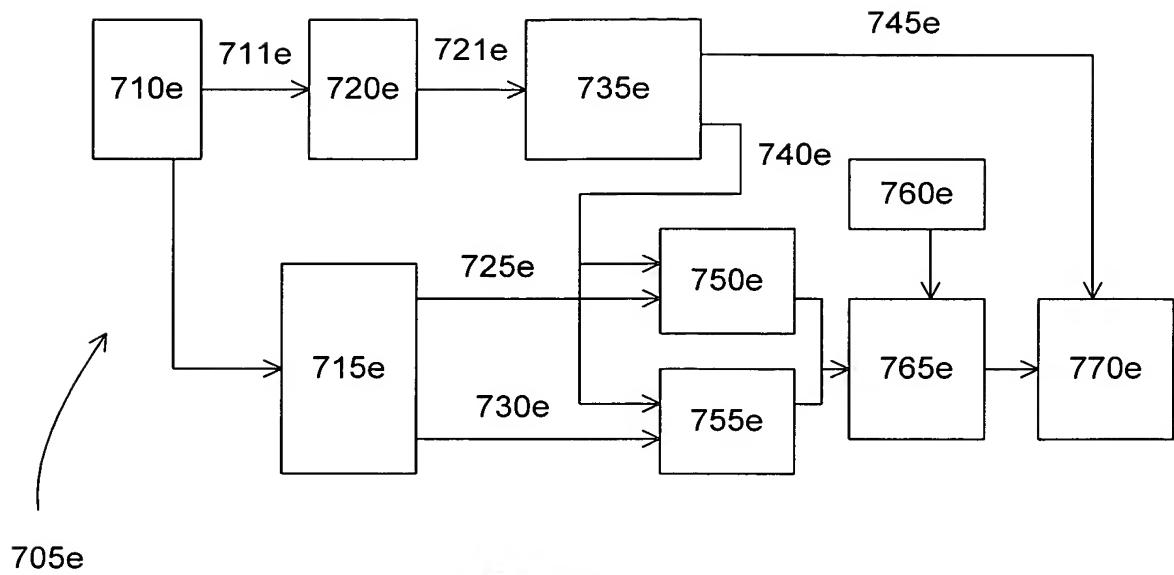


Fig.7e

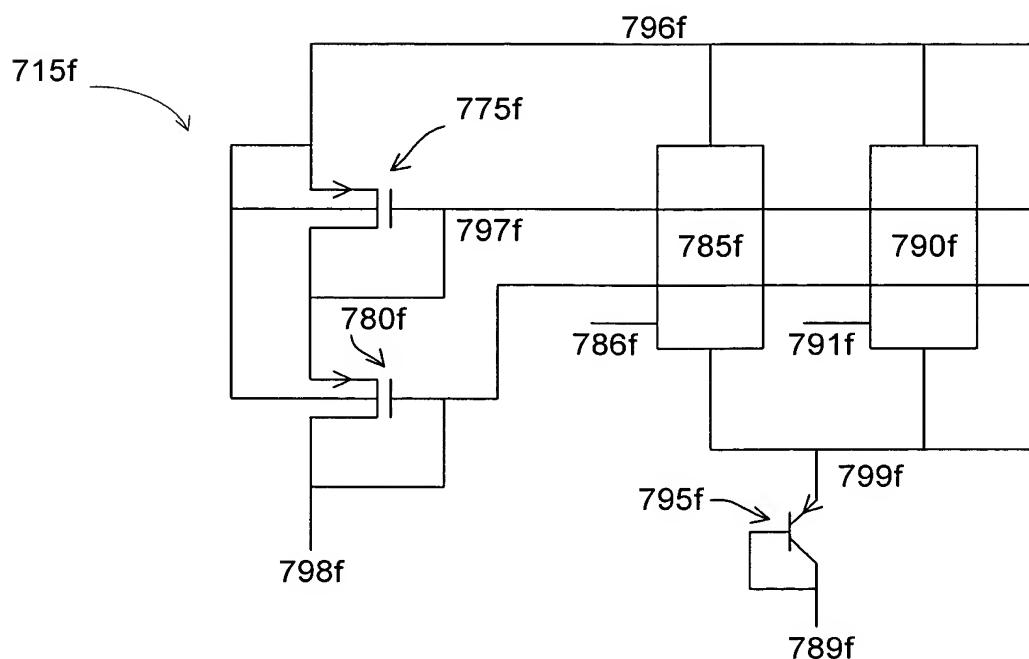
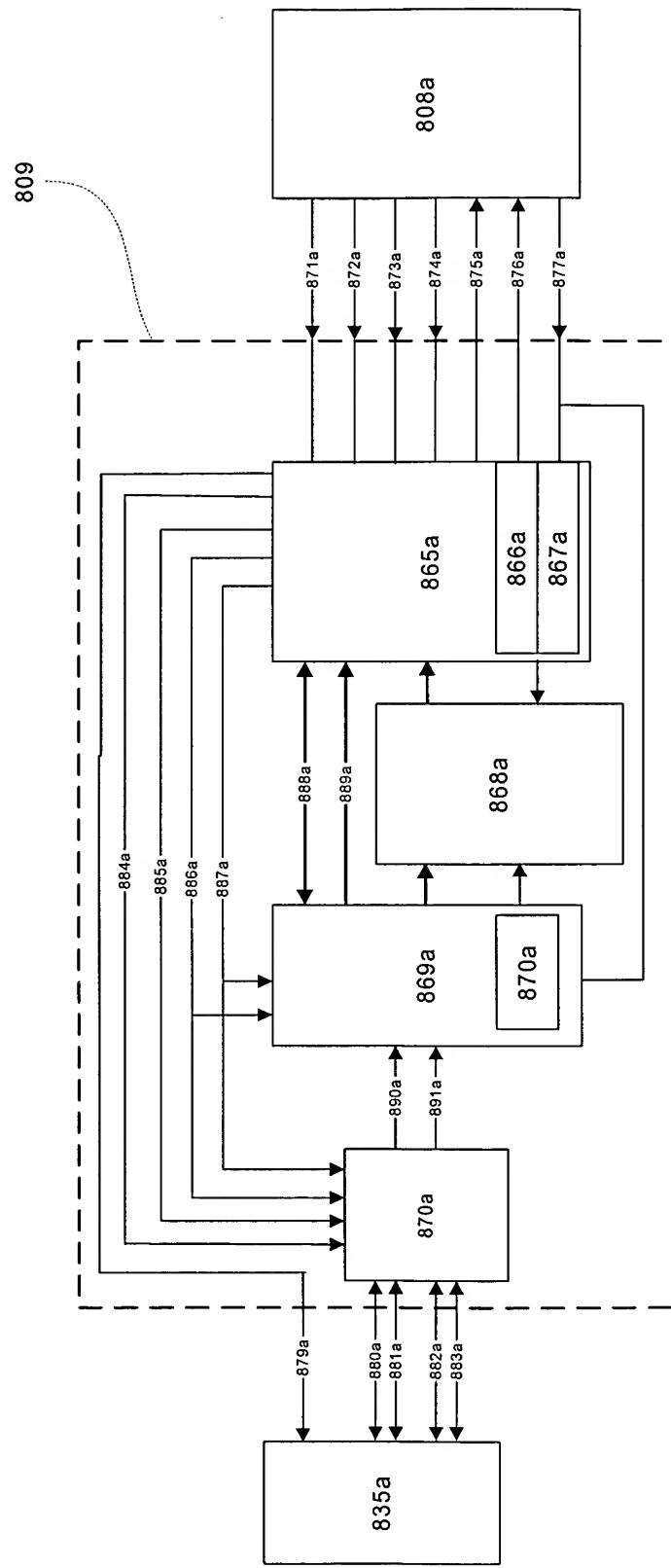


Fig.7f

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**Fig.8a**

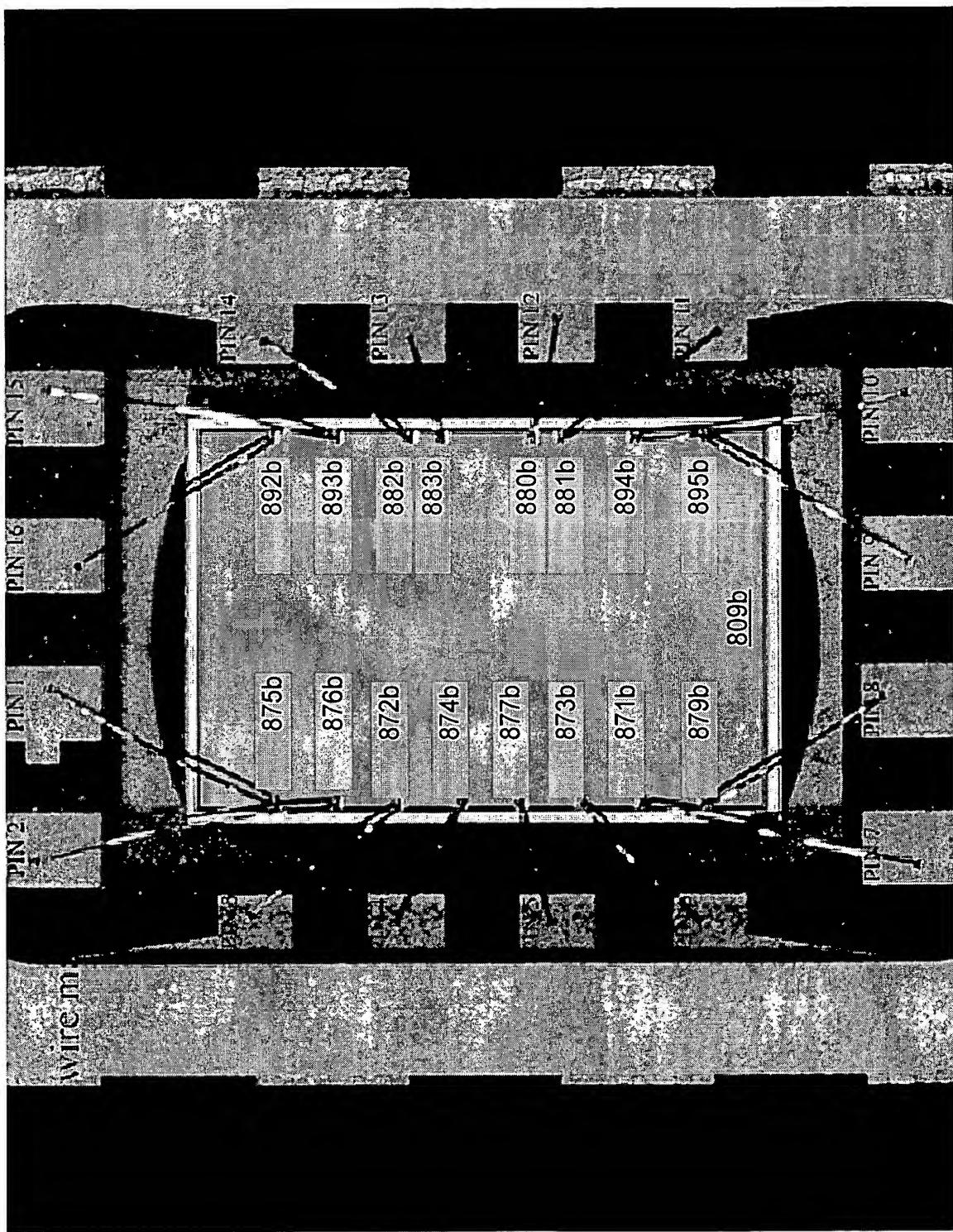


Fig.8b

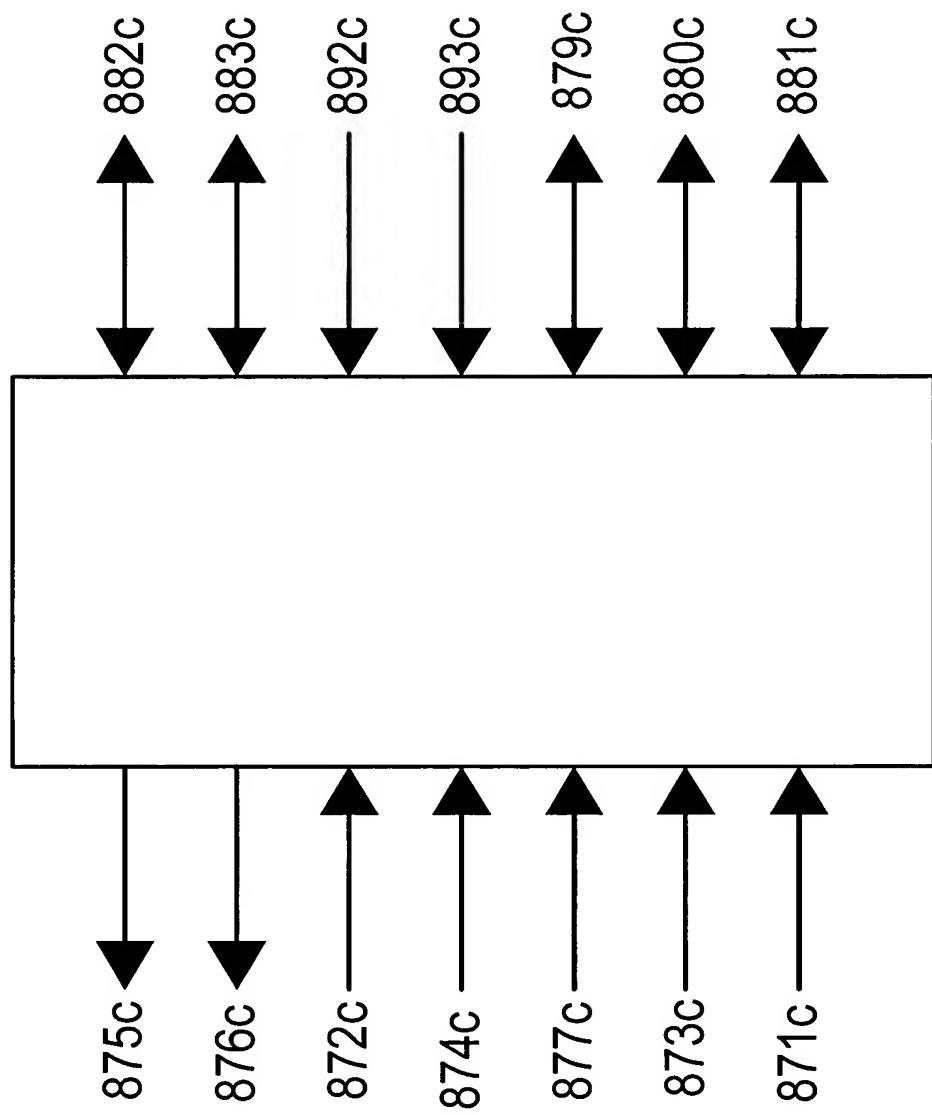


Fig.8c

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Microcontroller Signal Waveforms

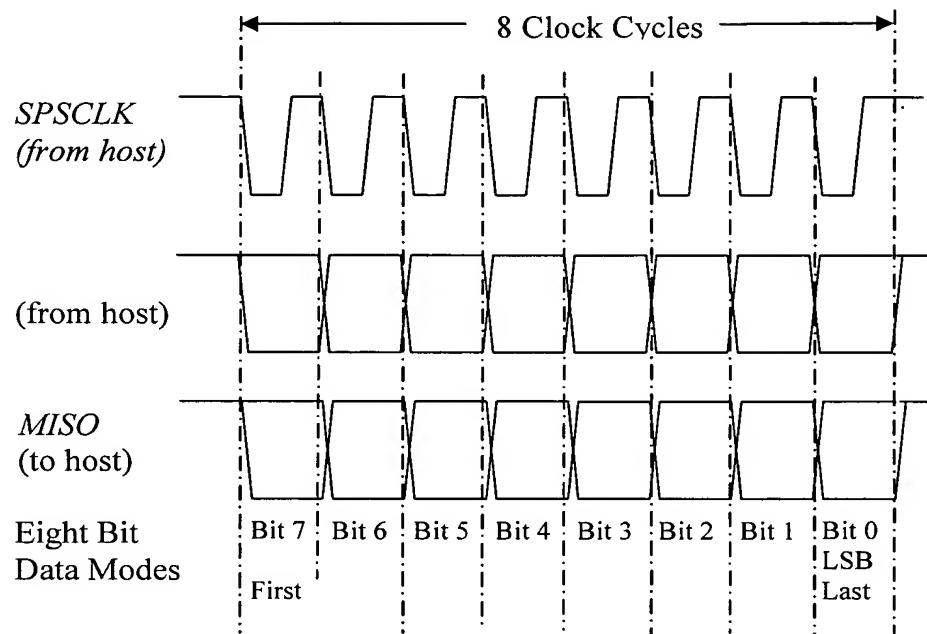


Fig.8d

LVDS Signal Waveforms –

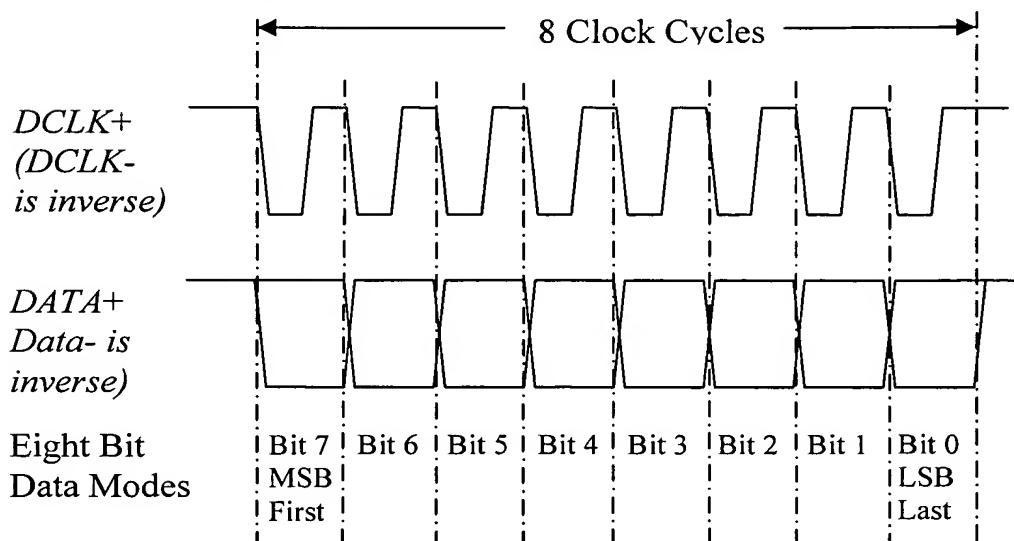
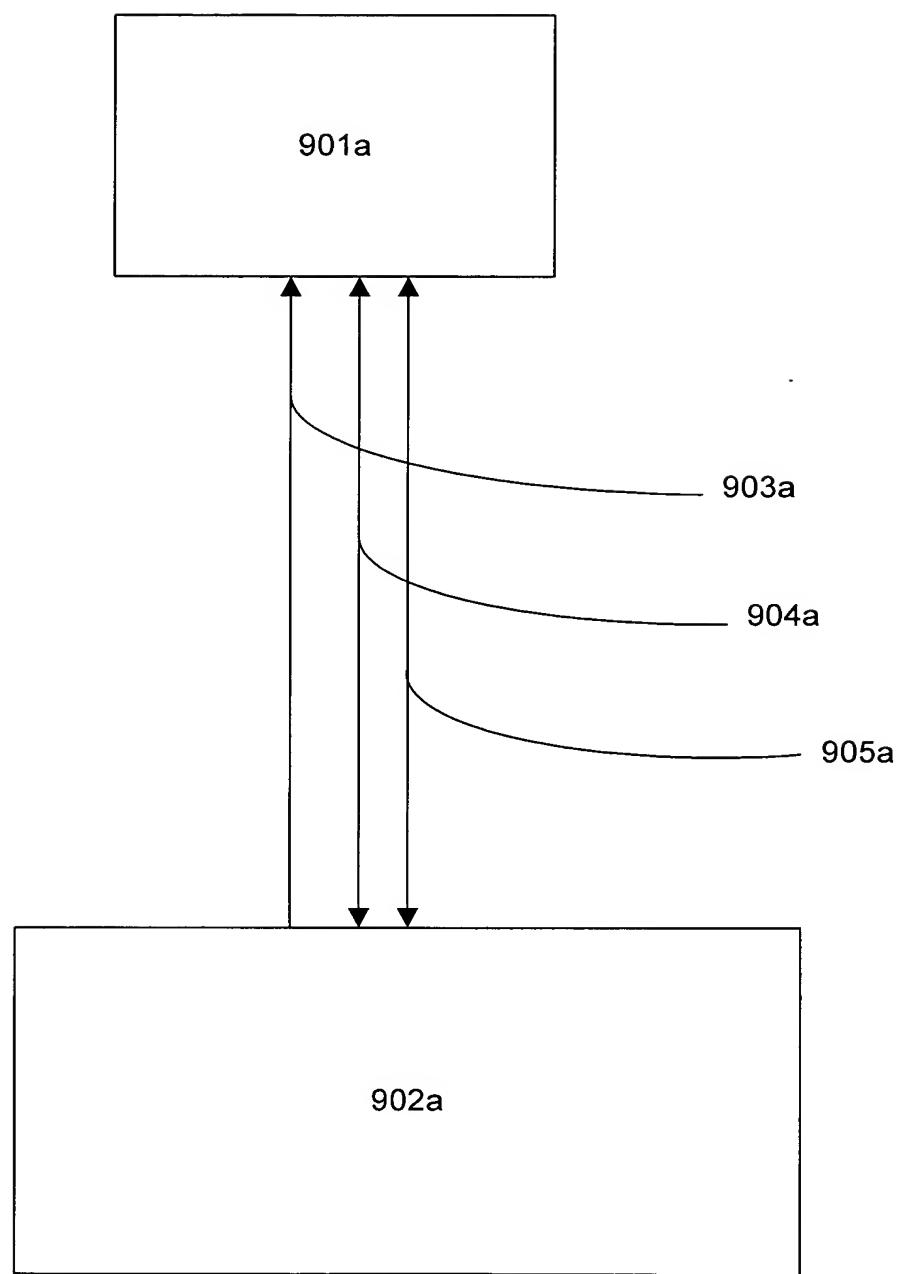


Fig.8e

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**Fig. 9a**

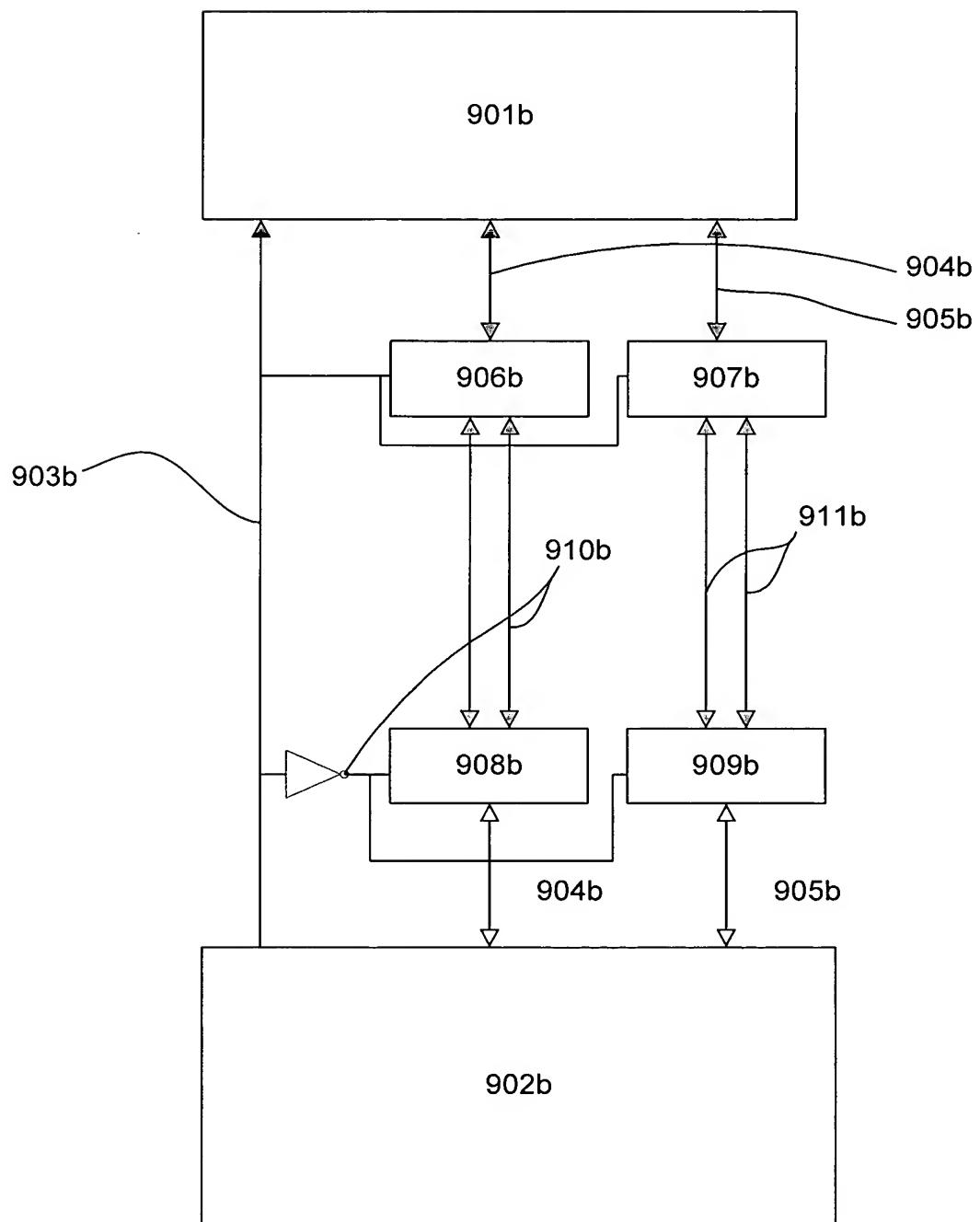
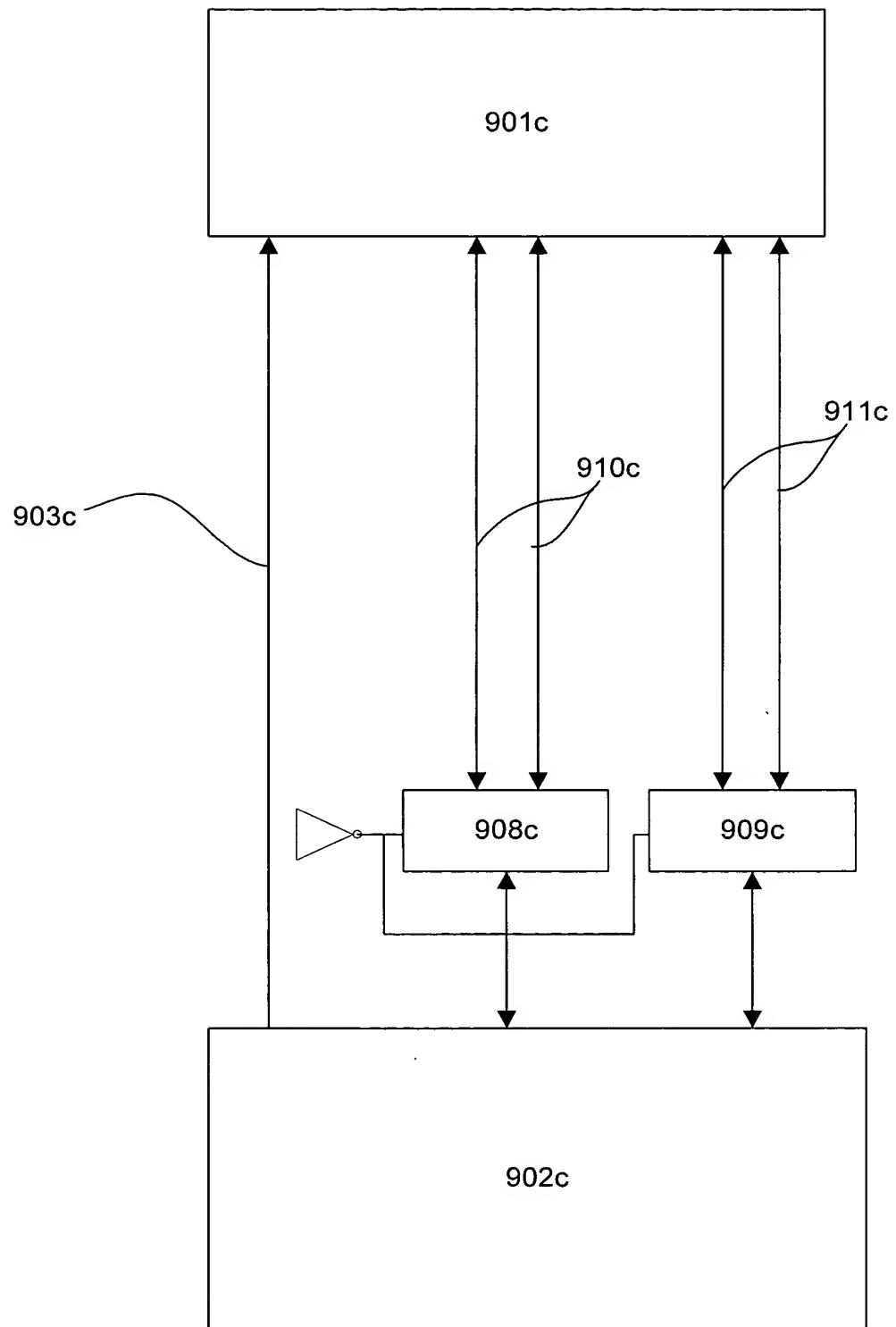


Fig. 9b



**Fig. 9c**

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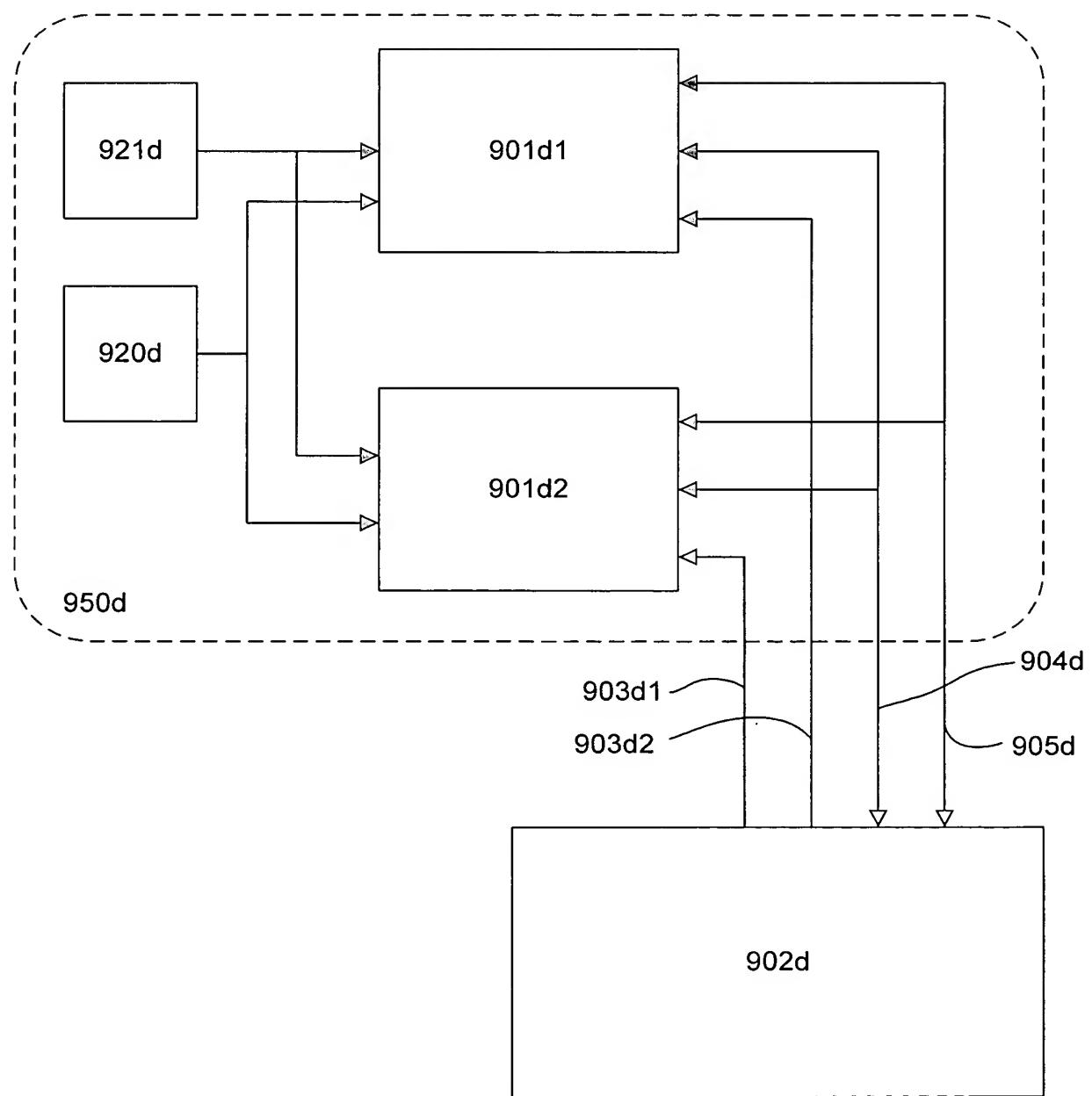


Fig. 9d

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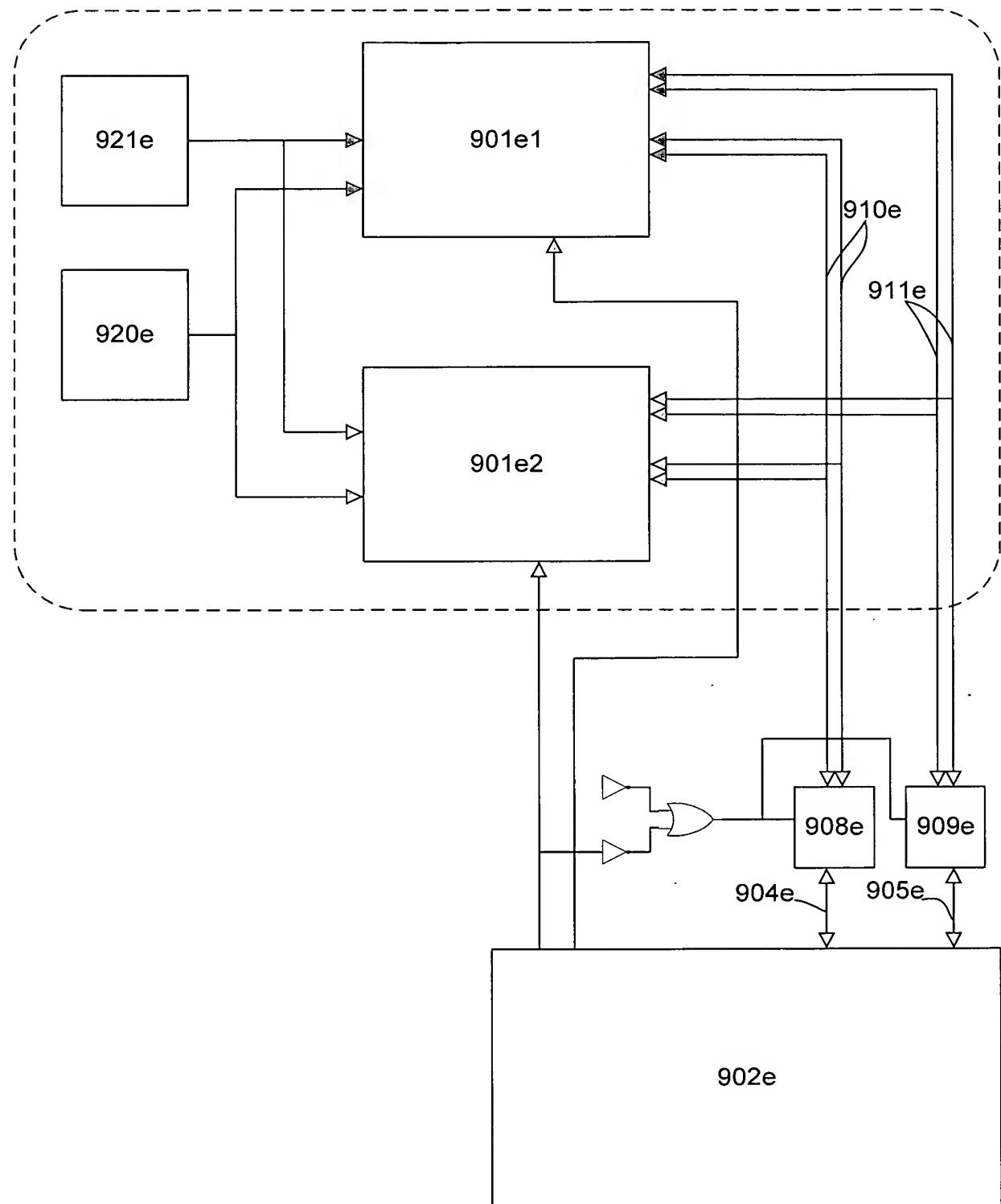


Fig. 9e

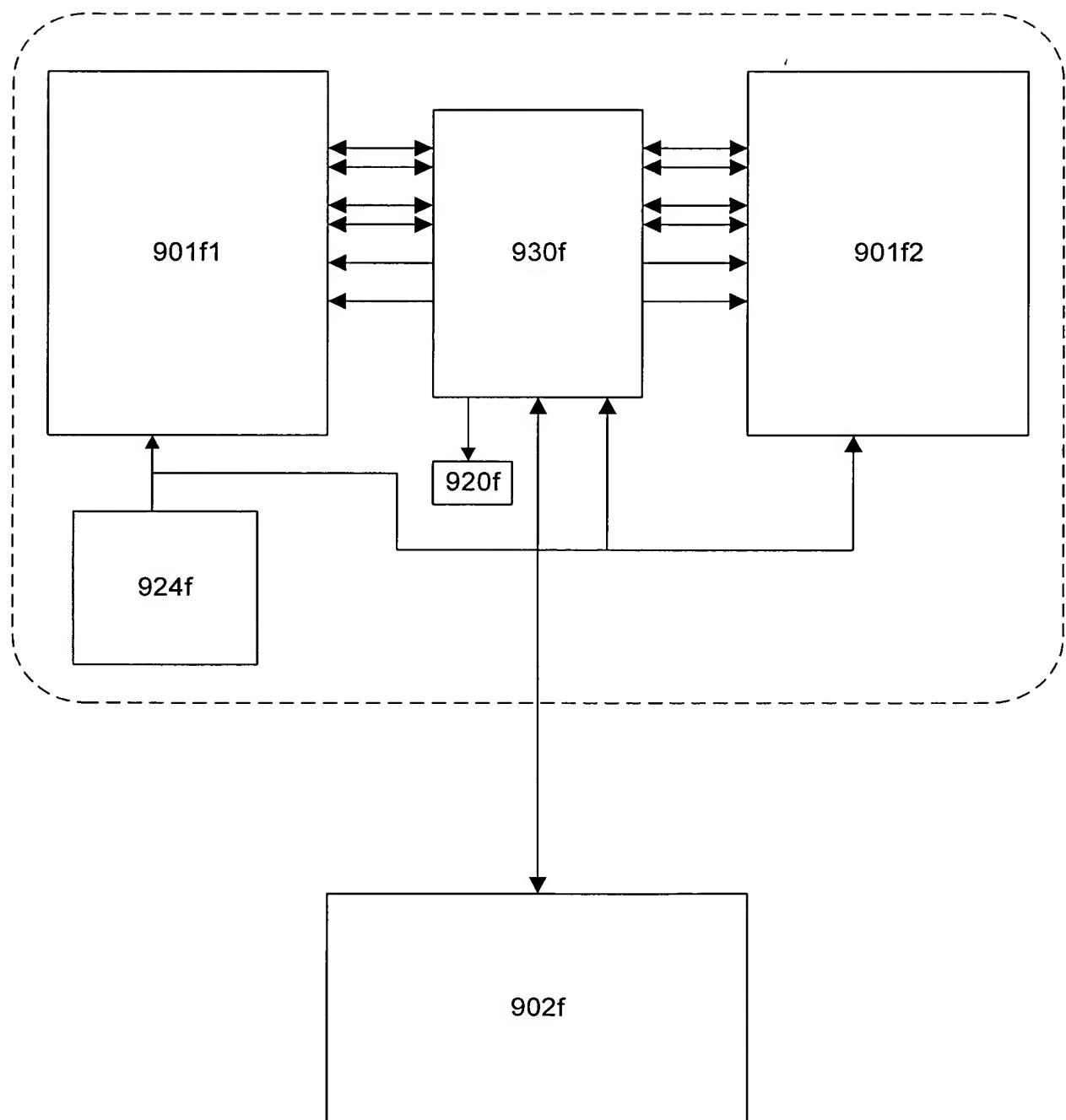


Fig. 9f

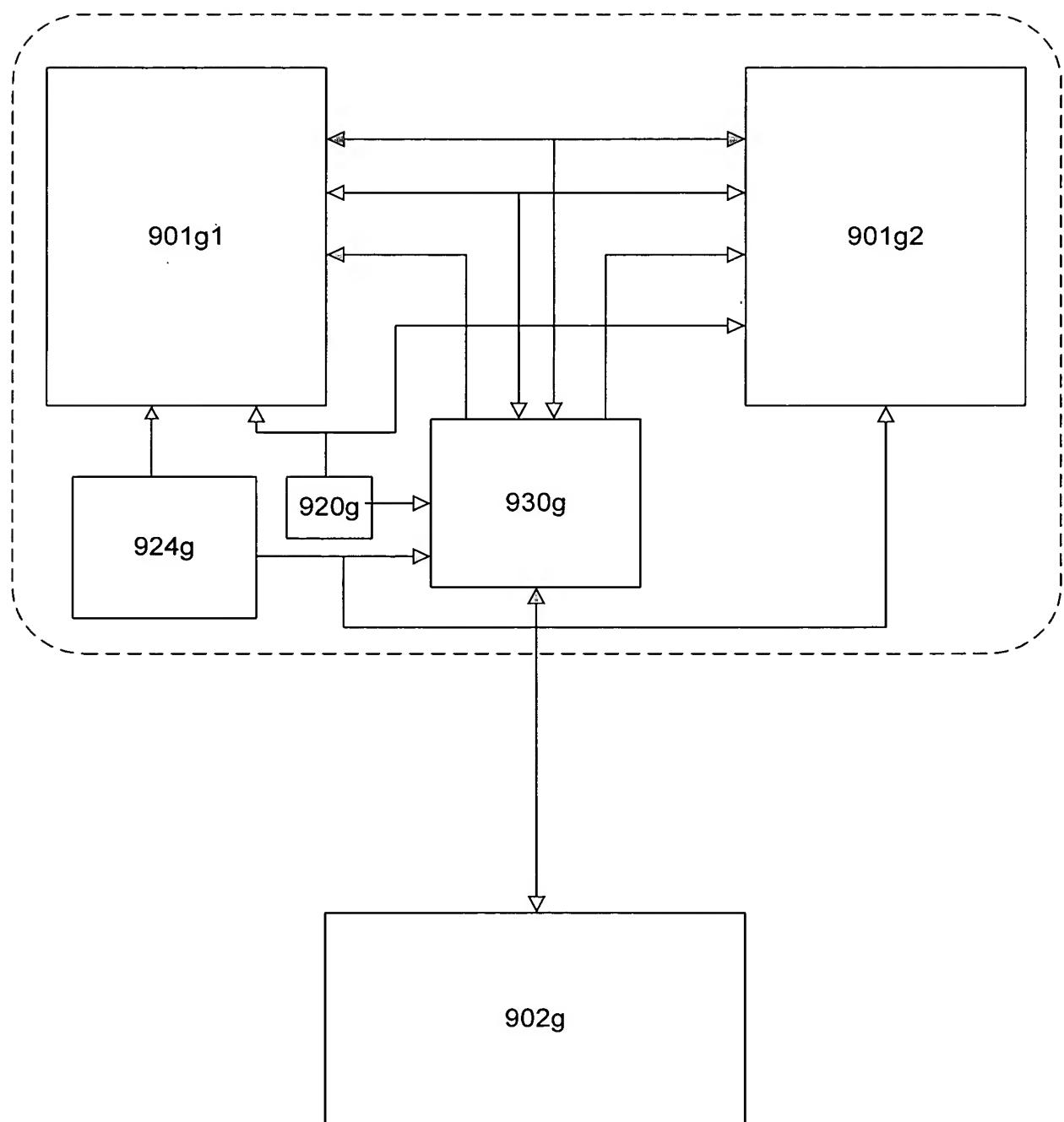
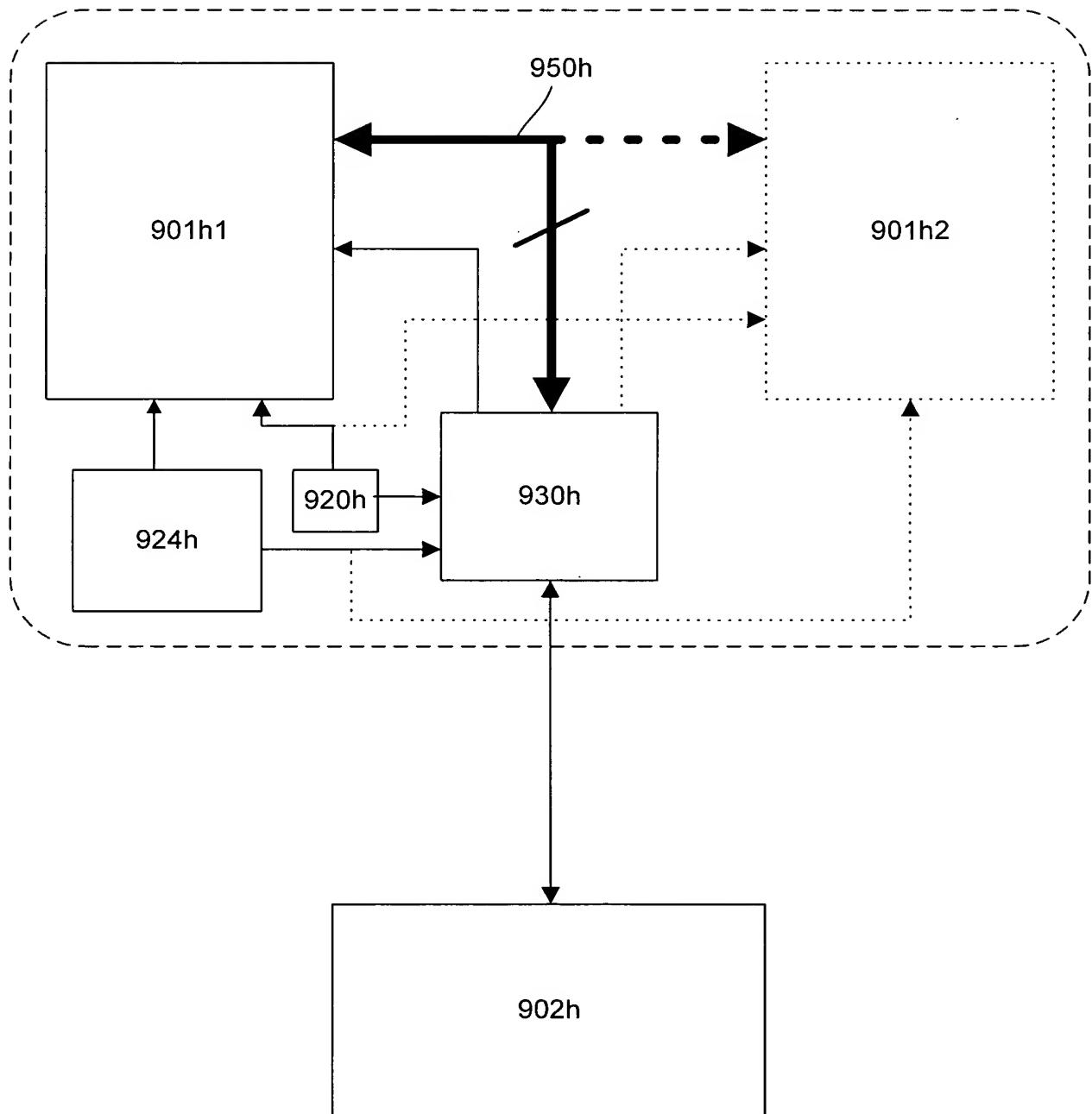
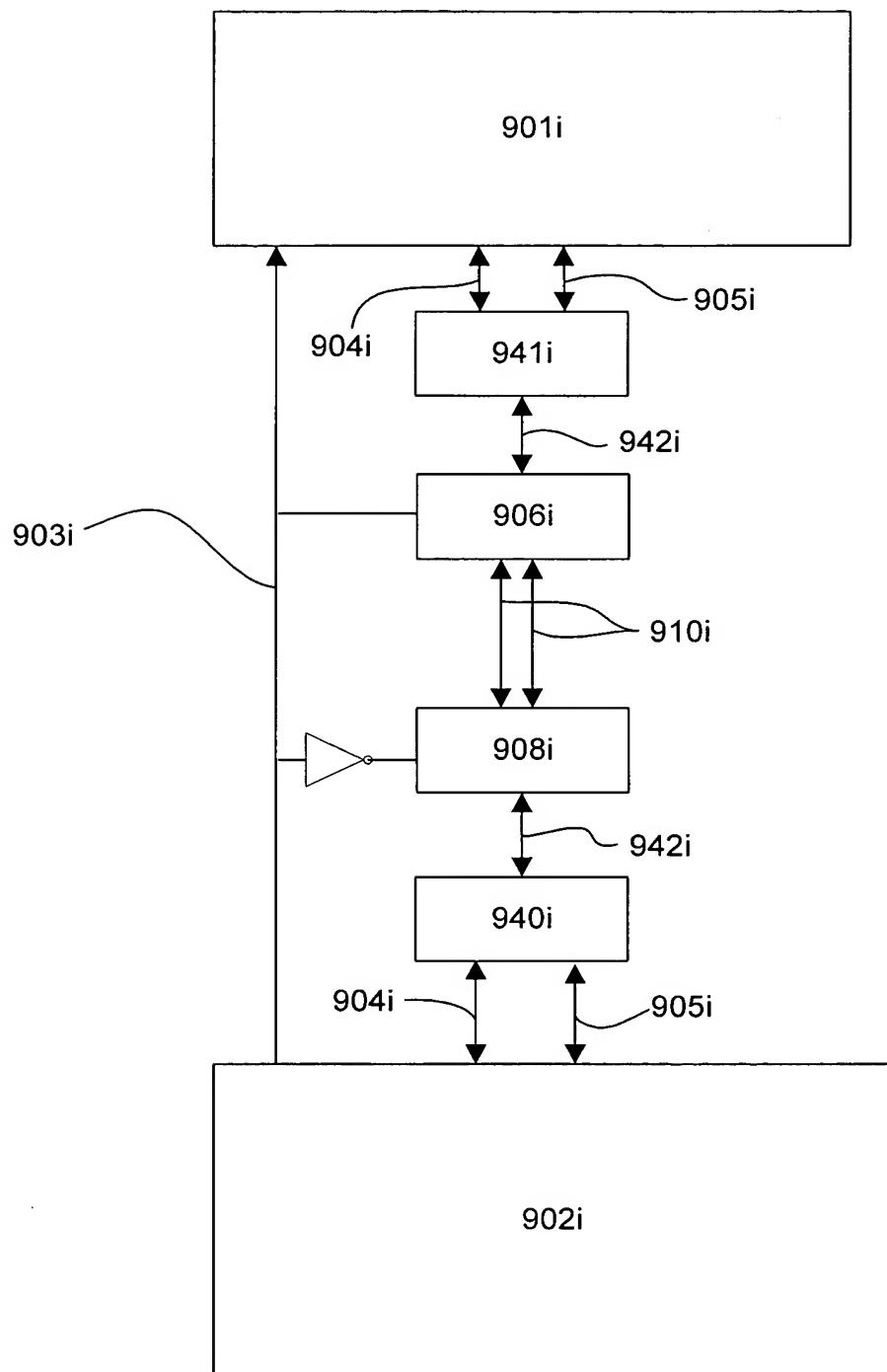


Fig. 9g



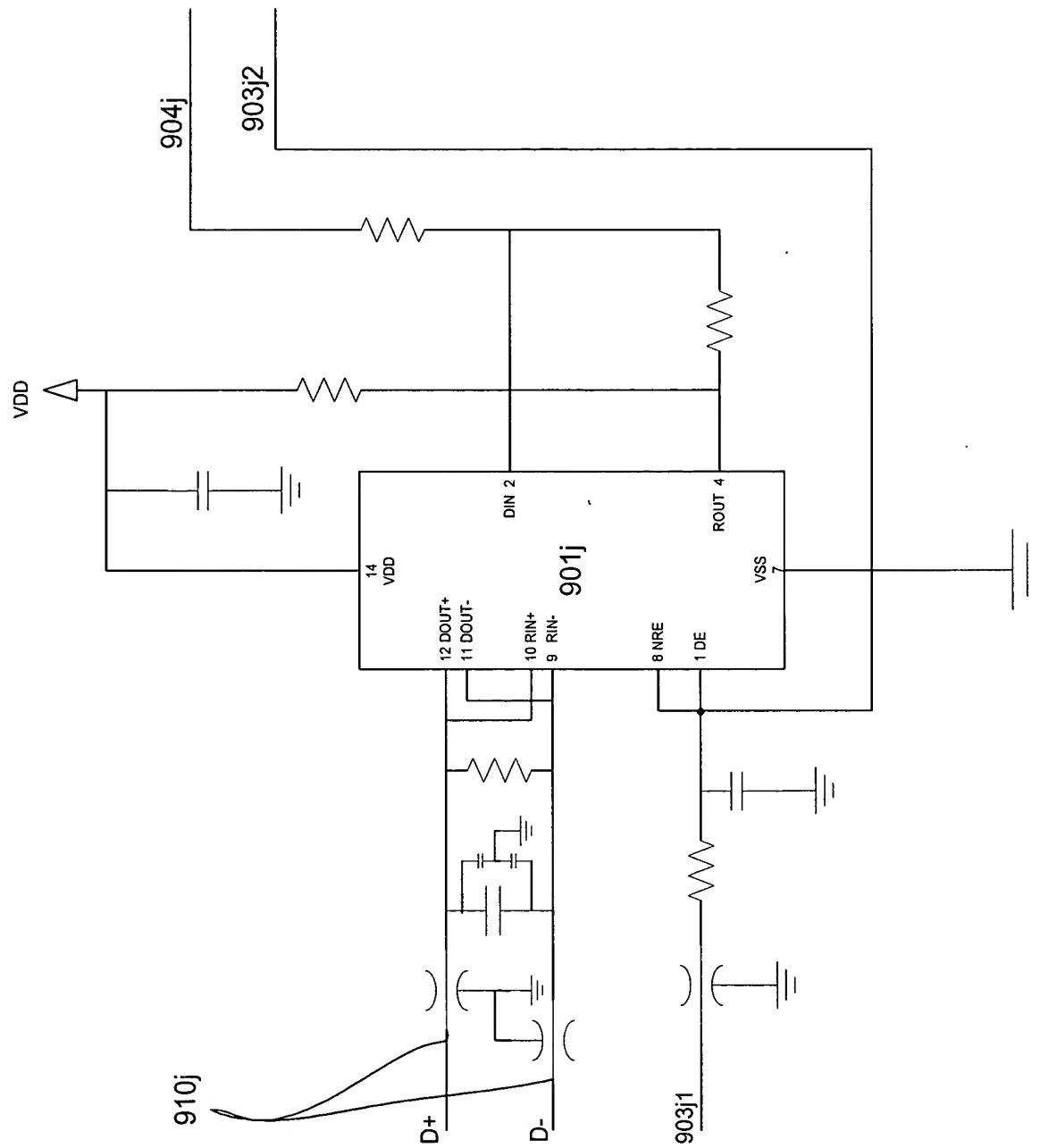
**Fig. 9h**

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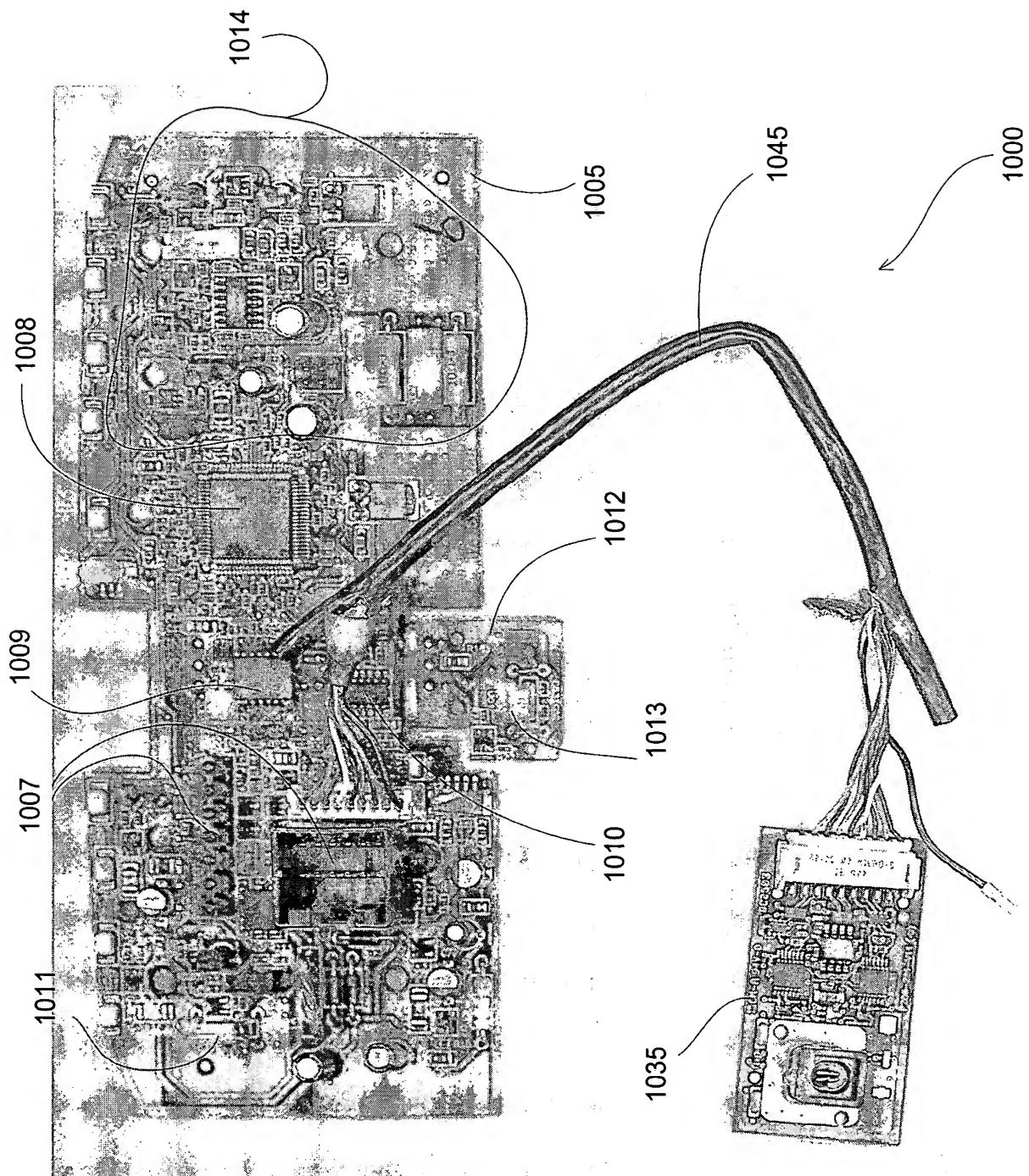
**Fig. 9i**

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**Fig. 9j**

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**Fig. 10**

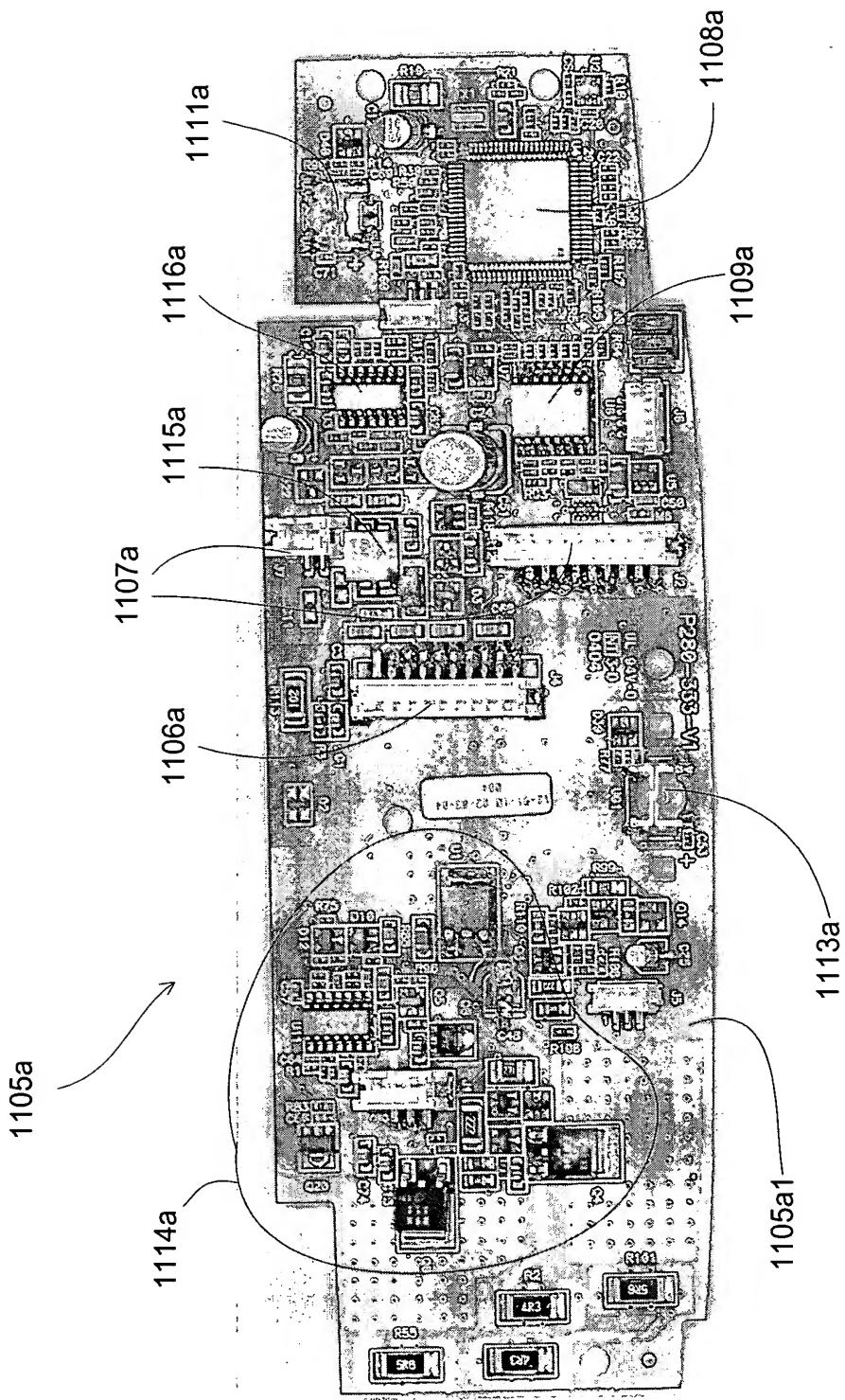
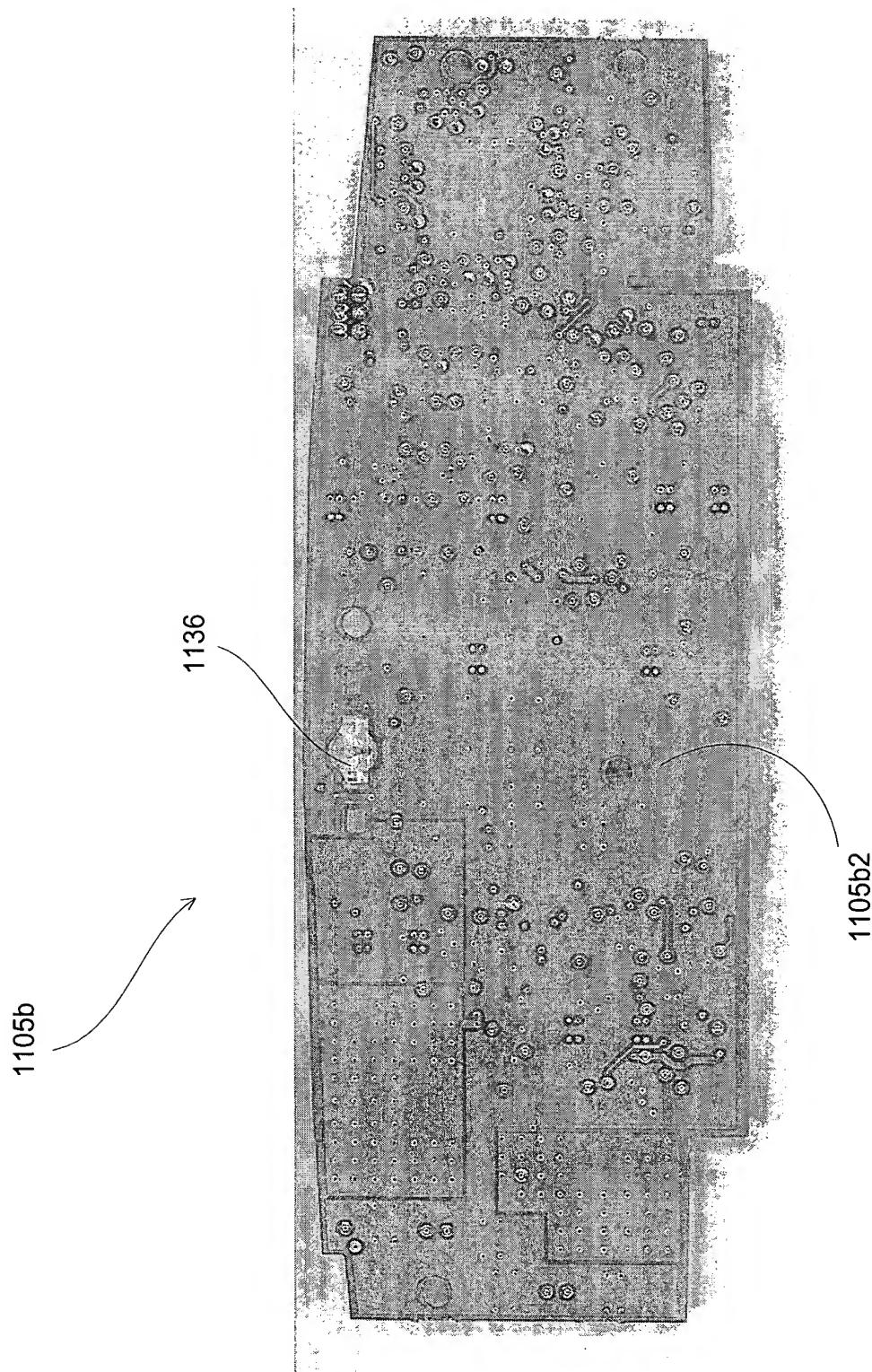


Fig. 11a

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**Fig. 11b**